

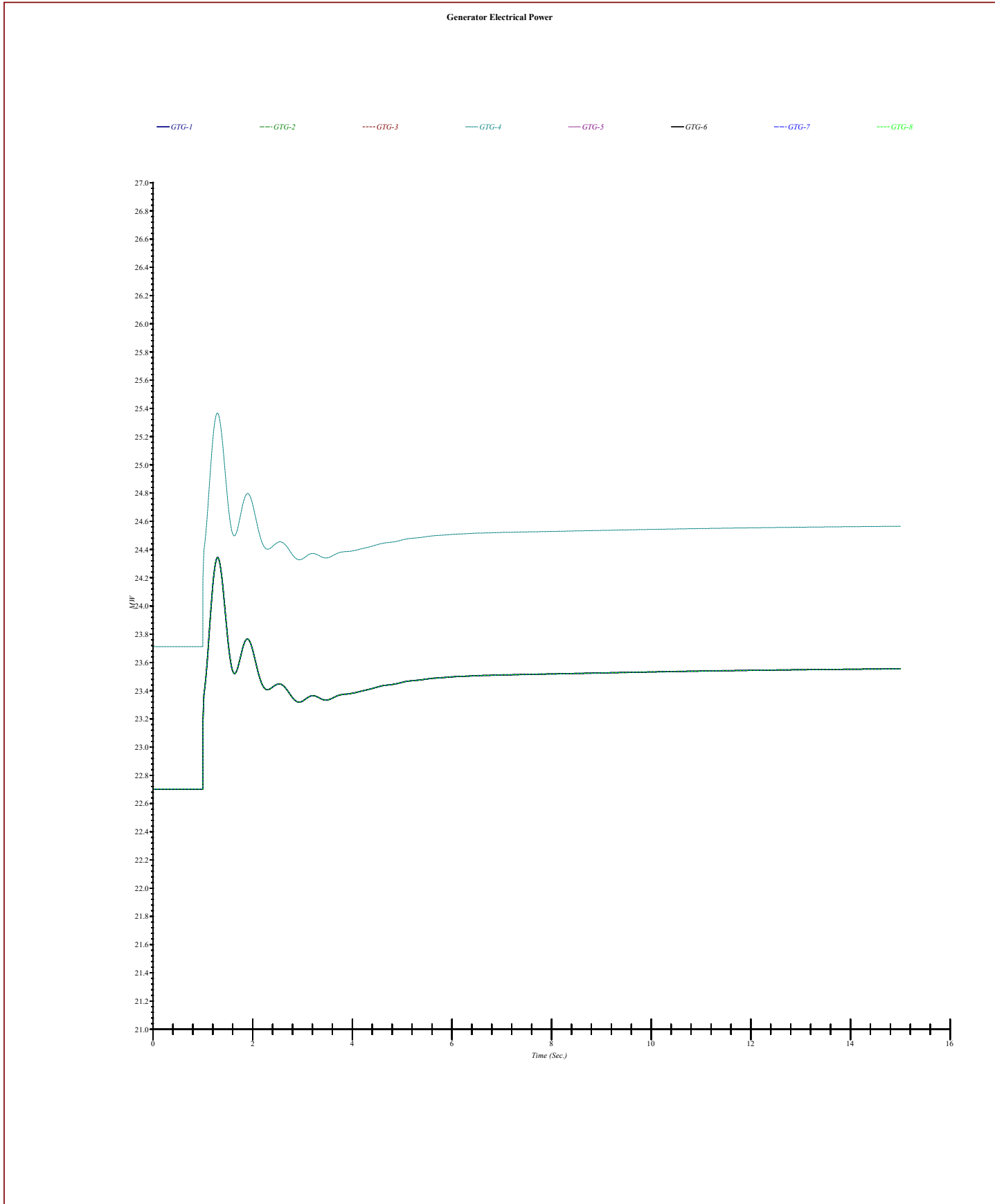
Project: Power System Study  
Location: Vadodara  
Contract:  
Engineer: Sayed Yassar

ETAP  
16.2.0C  
  
Study Case: TS

Date: 11-14-2019  
SN: ABB-INDIA9  
Revision: Base  
Config.: Normal\_C+s+I

Project File: C:\ETAP 1620\DORL\DORL  
Output Report: Untitled

## TRANSIENT STABILITY ANALYSIS



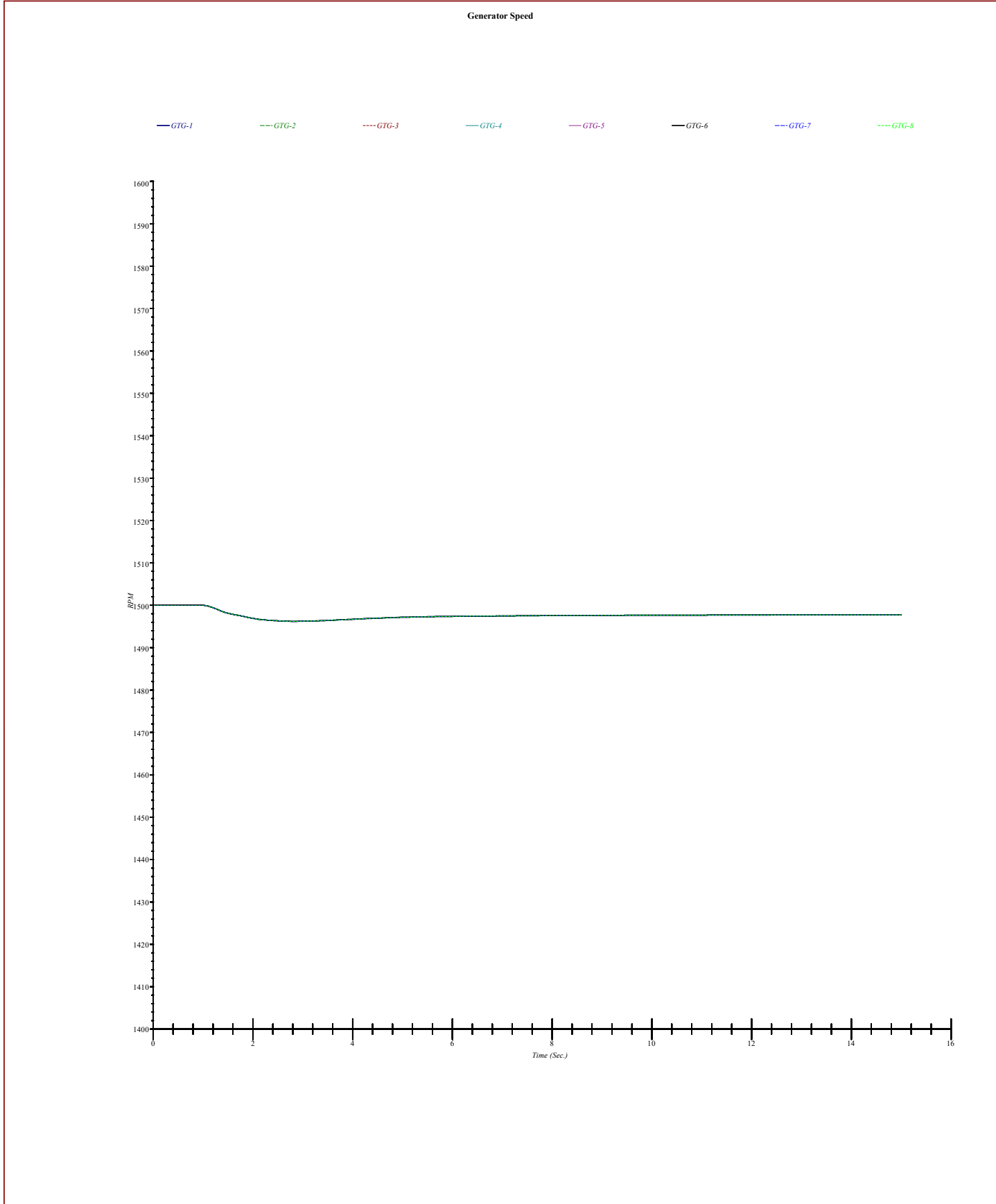
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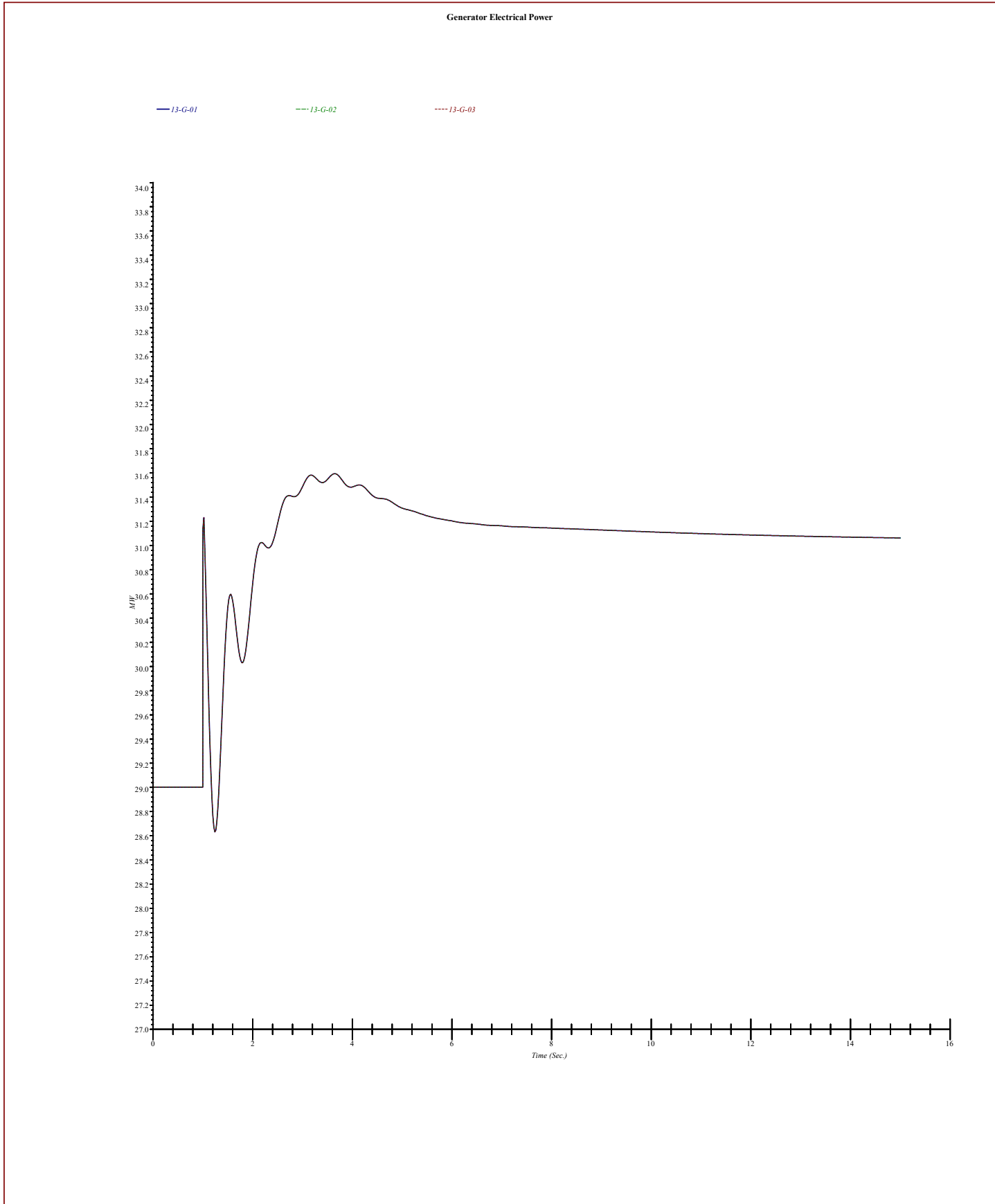
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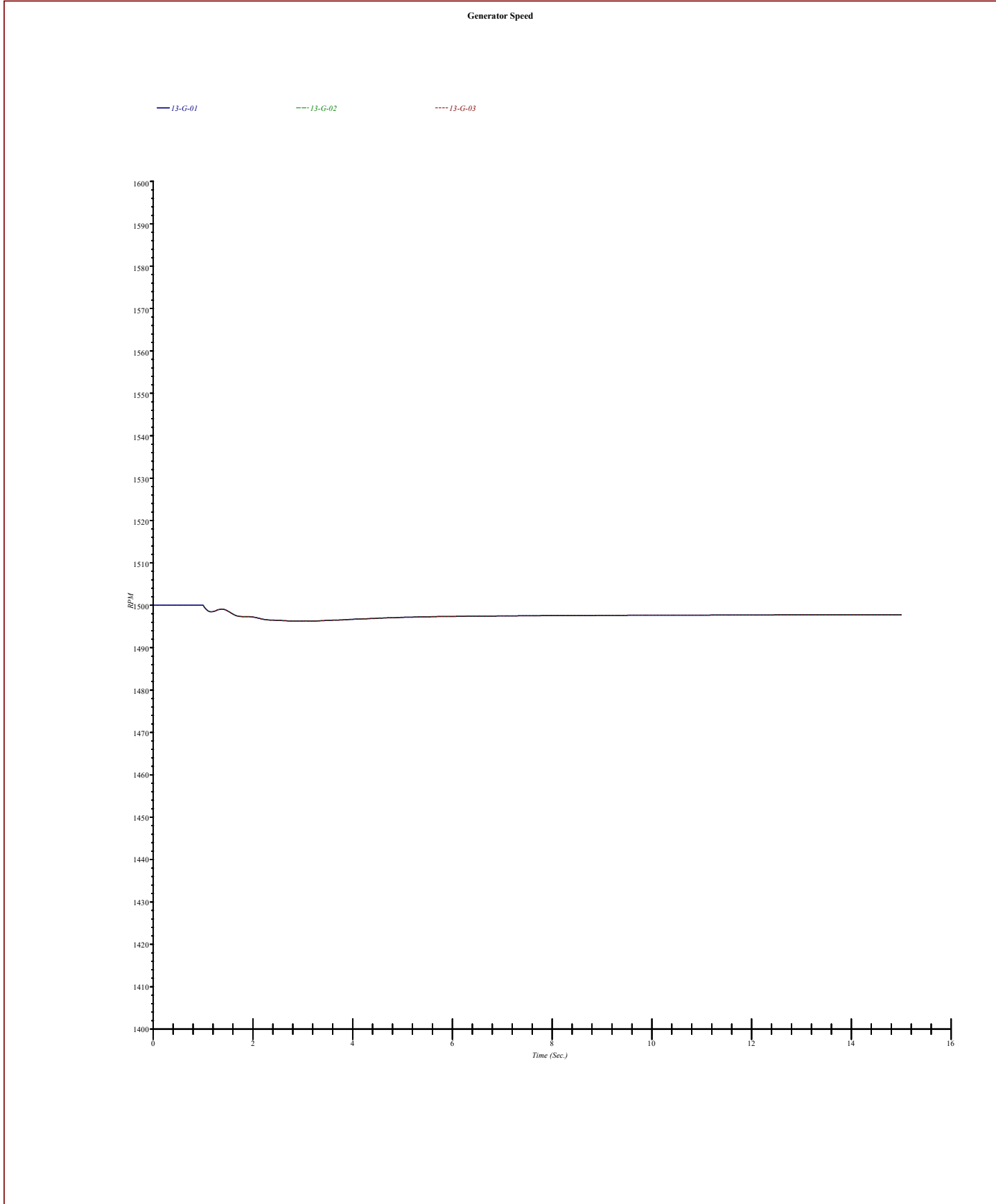
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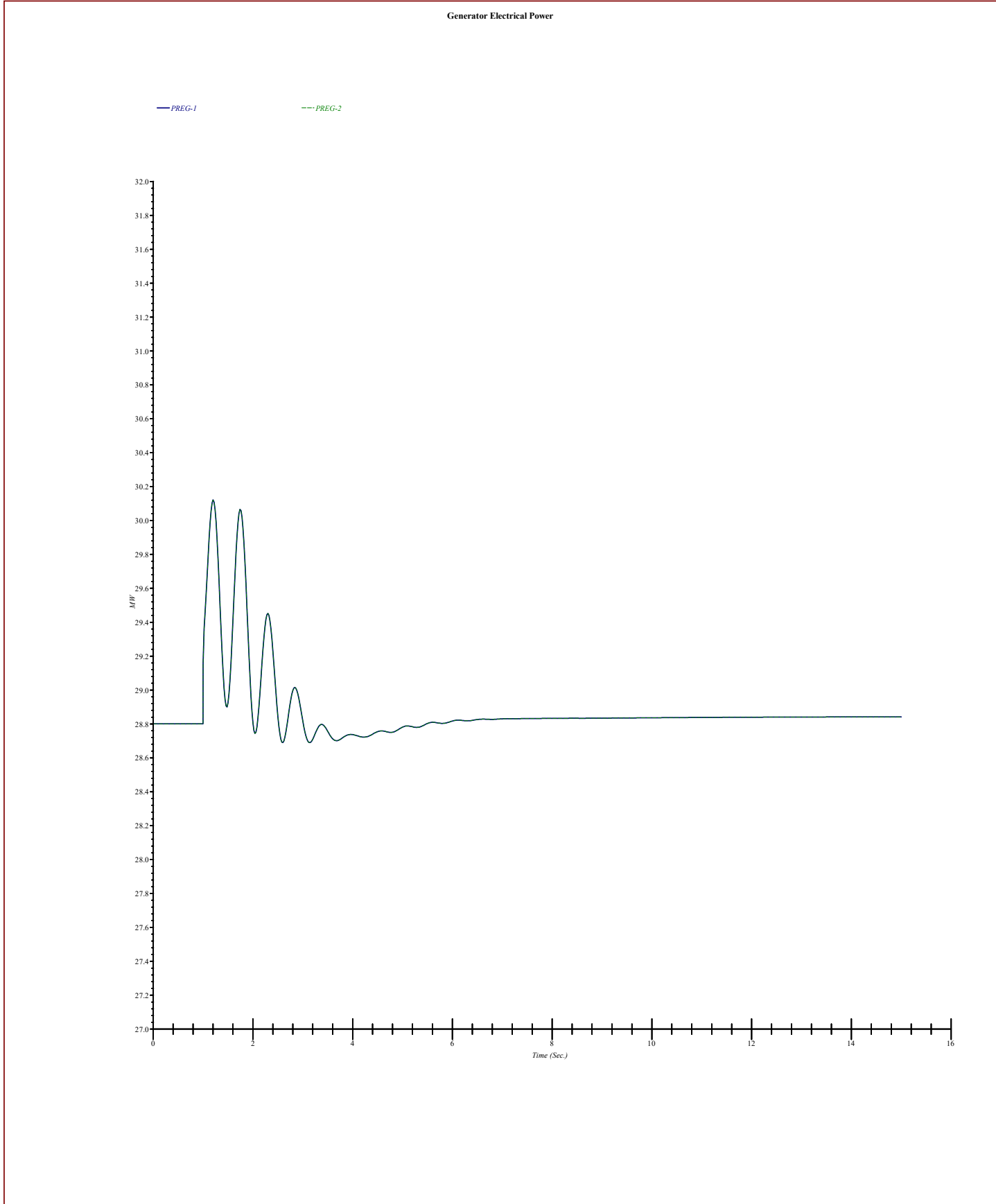
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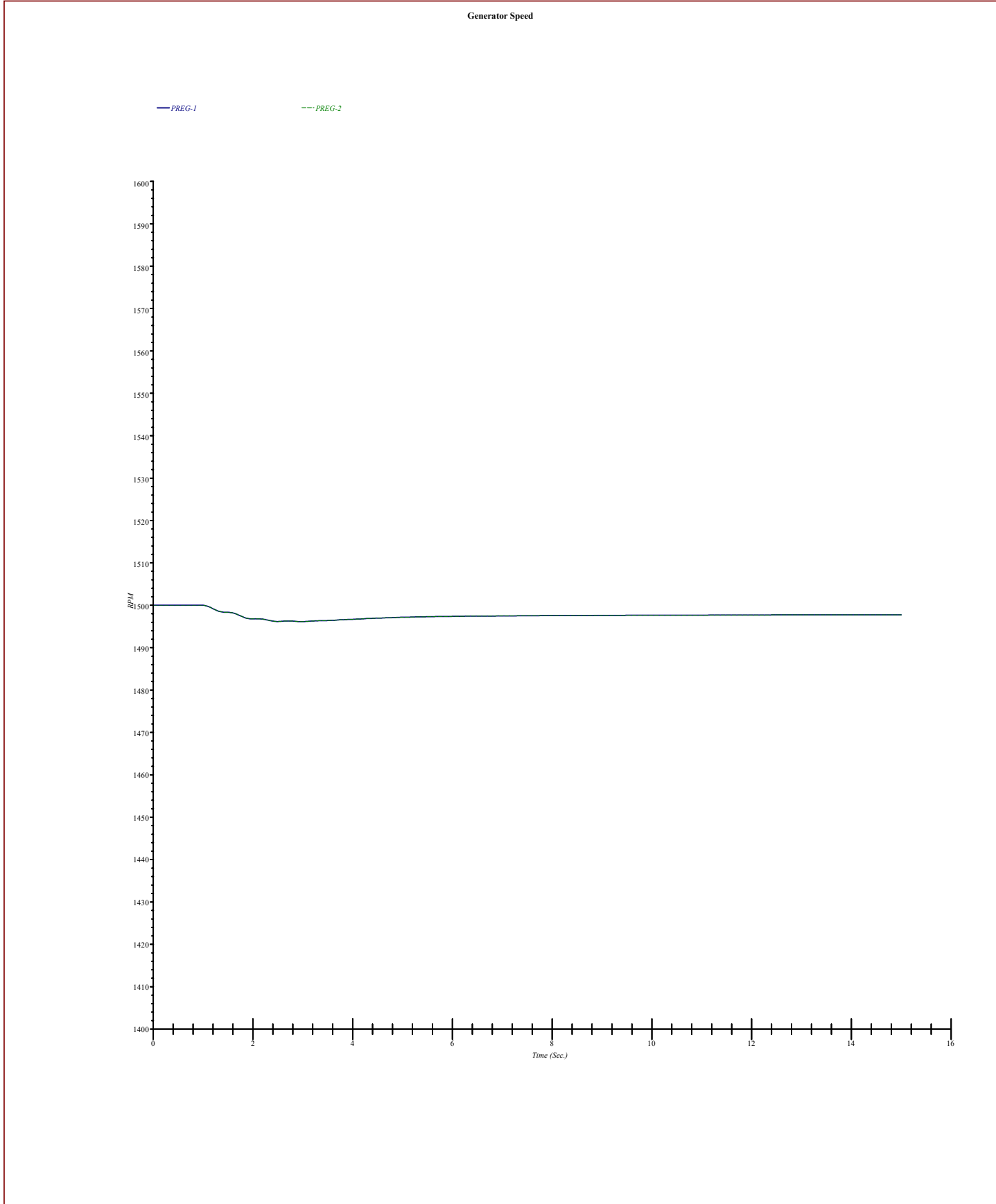
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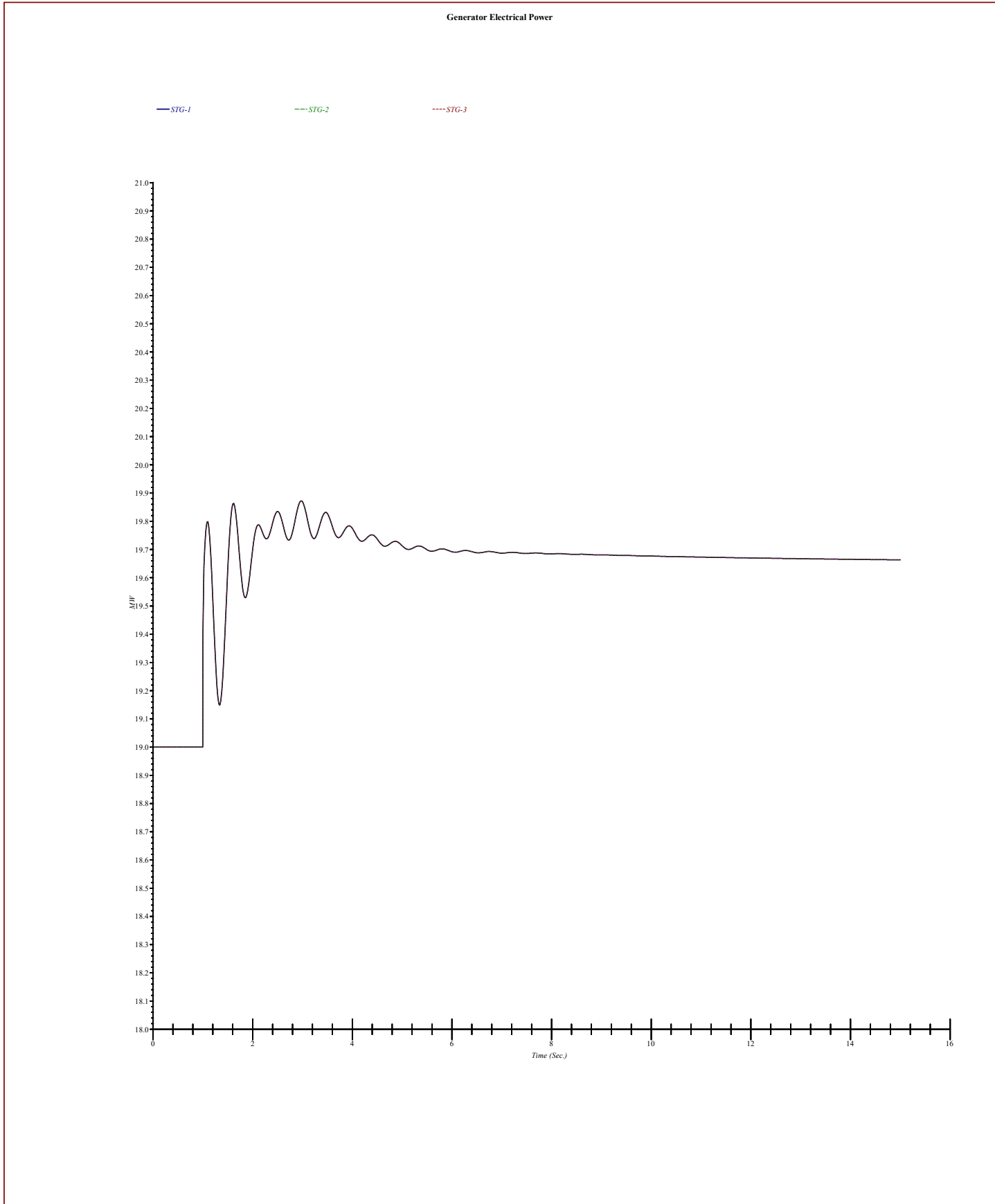
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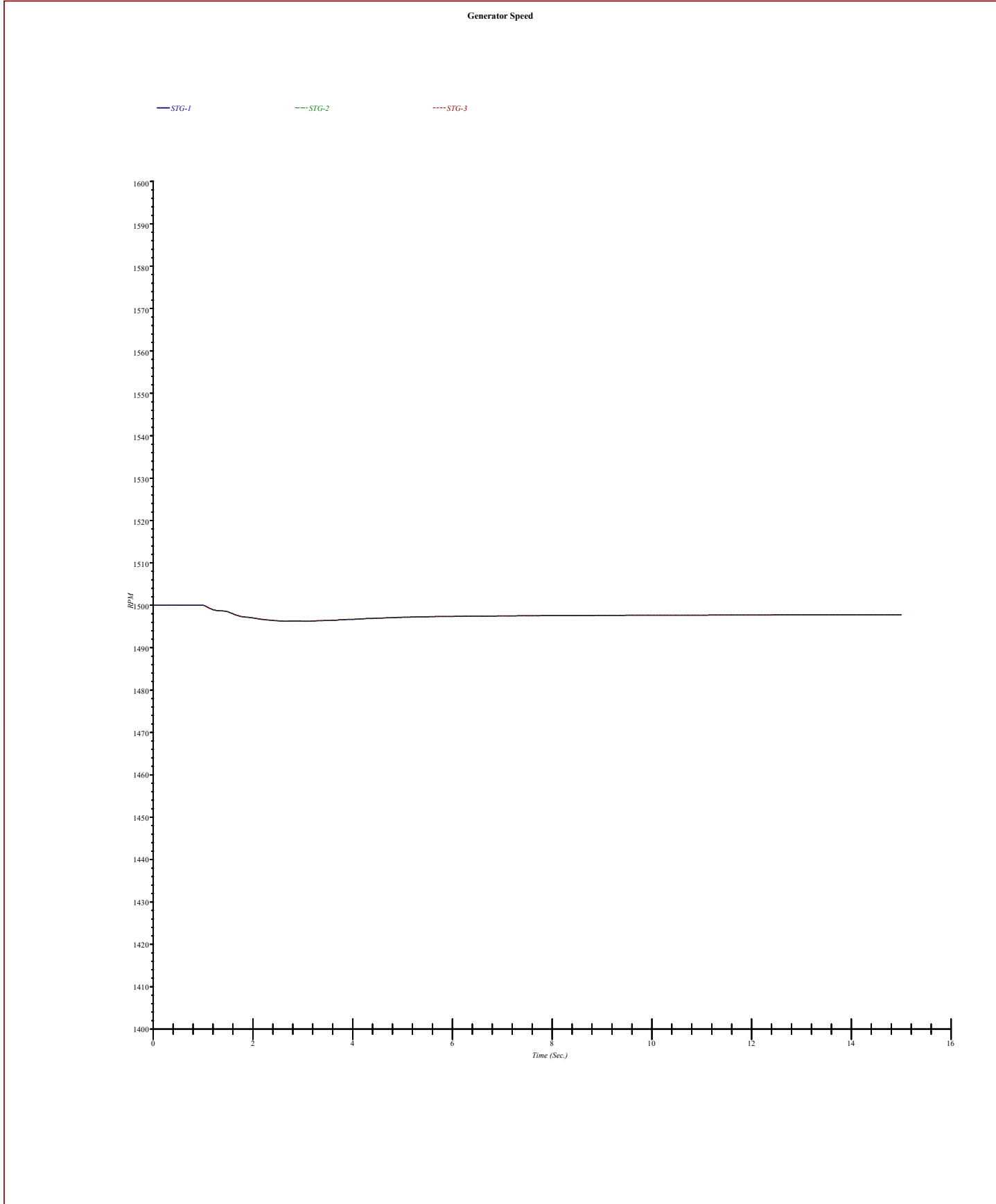
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